#### TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7SB66FU

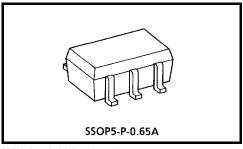
## Single Bus Switch

The TC7SB66FU is a low on-resistance, high-speed CMOS 1-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

P-MOS and N-MOS channel block means the device is suitable for analog signal transmission.

All inputs are equipped with protector circuits to protect the device from static discharge.

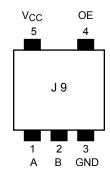


Weight: 0.006 g (typ.)

#### **Features**

- Operating voltage:  $V_{CC} = 2 \sim 5.5 \text{ V}$
- High speed operation:  $t_{pd} = 0.25 \text{ ns (max)}$
- Ultra-low on resistance:  $RON = 5 \Omega$  (typ.)
- ESD performance: Machine model  $\geq \pm 200 \text{ V}$ 
  - Human body model  $\geq \pm 2000 \text{ V}$
- $\bullet~$  High noise margin:  $V_{\mbox{\scriptsize NIL}}$  =  $V_{\mbox{\scriptsize NIH}}$  = 28%  $V_{\mbox{\scriptsize CC}}$  (min)
- Power-down protection for inputs (control inputs only)
- Package: USV

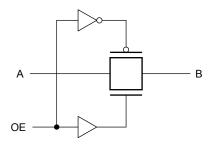
## Pin Assignment (top view)



#### **Truth Table**

Inputs	Function
OE	ranction
Н	A port = B port
Ĺ	Disconnect

## **System Diagram**



## **Absolute Maximum Ratings (Note)**

Chara	acteristics	Symbol	Rating	Unit	
Power supply vol	age	V <sub>CC</sub>	-0.5~7.0	V	
Control pin input	/oltage	V <sub>IN</sub>	-0.5~7.0	V	
Switch terminal I/O voltage		VS	-0.5~V <sub>CC</sub> + 0.5	V	
Clump diode current	Control input pin	luz	-50	mA	
	Switch terminal	- lık	±50	IIIA	
Switch I/O current		IS	128	mA	
Power dissipation		PD	P <sub>D</sub> 200		
DC V <sub>CC</sub> /GND cur	rent	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA	
Storage temperature		T <sub>stg</sub>	-65~150	°C	

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	V <sub>CC</sub>	2.0~5.5	V
Control pin input voltage	V <sub>IN</sub>	0~5.5	٧
Switch I/O voltage	Vs	0~V <sub>CC</sub>	٧
Operating temperature	T <sub>opr</sub>	-40~85	°C
Control pin input rise/fall time	dt/dv	0~10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.

## **Electrical Characteristics**

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## DC Characteristics ( $Ta = -40 \sim 85$ °C)

Character	ristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Typ. (Note 1)	Max	Unit
Control pin input	"H" level	V <sub>IH</sub>	_	2.0~5.5	0.7 × V <sub>CC</sub>	_	_	V
voltage "L" level V <sub>IL</sub>		V <sub>IL</sub>	_	2.0~5.5	_	_	0.3 × V <sub>CC</sub>	V
Control pin input I current	eakage	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5 V	2.0~5.5	_	_	±1.0	μΑ
Off-state leakage (switch off)	current	I <sub>SZ</sub>	A, B = 0~V <sub>CC</sub> , OE = GND	2.0~5.5	_	_	±1.0	μА
		R <sub>ON</sub>	V <sub>IS</sub> = 0 V, I <sub>IS</sub> = 30 mA	4.5	_	3	7	
			V <sub>IS</sub> = 4.5 V, I <sub>IS</sub> = 30 mA	4.5	_	5	15	
ON resistance (Note 2)	$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$		4.5	_	6	12		
	V <sub>IS</sub> = 0 V, I <sub>IS</sub> = 24 mA		3.0	_	4	9	Ω	
	$V_{IS} = 3 \text{ V}, I_{IS} = 24 \text{ mA}$		3.0	_	7	20		
			$V_{IS} = 0 \text{ V}, I_{IS} = 8 \text{ mA}$	2.0	_	6	12	
			V <sub>IS</sub> = 2 V, I <sub>IS</sub> = 8 mA	2.0		10	30	
Quiescent supply	current	I <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$	5.5		_	10	μА

Note 1: The typical values are at  $Ta = 25^{\circ}C$ .

Note 2: Apply the specified current to the switch, then measure the voltages on pins A and B. The on-resistance is the lower of the two.

## AC Characteristics ( $Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Drong getion delay time	4		2.0	_	0.5	
Propagation delay time (bus to bus)	t <sub>pLH</sub>	Figure 1, Figure 2 (Note)	$3.3\pm0.3$	_	0.35	ns
(bus to bus)	t <sub>pHL</sub>	L	5.0 ± 0.5	_	0.25	
Output enable time	t <sub>pZL</sub>	Figure 1, Figure 3	2.0	_	8	ns
			$3.3\pm0.3$	_	5	
			$5.0 \pm 0.5$	_	4.5	
Output disable time	t <sub>pLZ</sub>	Figure 1, Figure 3	2.0	_	8	
			$3.3\pm0.3$	_	6.5	ns
	t <sub>pHZ</sub>		$5.0 \pm 0.5$	_	5	

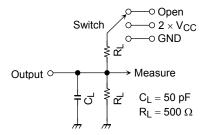
Note: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

## **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Тур.	Unit
Control pin input capacitance	C <sub>IN</sub>		(Note)	5.0	3	pF
Switch terminal capacitance	C <sub>I/O</sub>	OE = GND	(Note)	5.0	10	pF

Note: Guaranteed by design.

## **AC Test Circuit**



Parameter	Switch
t <sub>pLH</sub> , t <sub>pHL</sub>	Open
t <sub>pLZ</sub> , t <sub>pZL</sub>	$2\times V_{CC}$
t <sub>pHZ</sub> , t <sub>pZH</sub>	GND

Figure 1

## **AC Waveform**

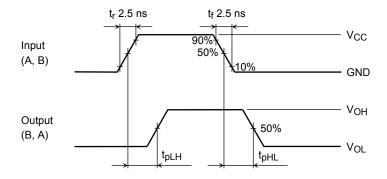


Figure 2 tpLH, tpHL

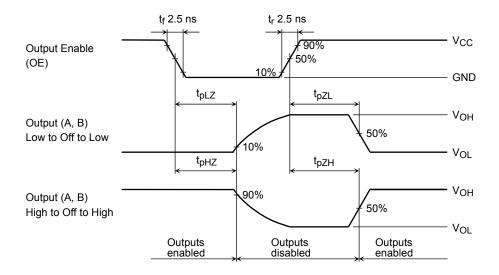
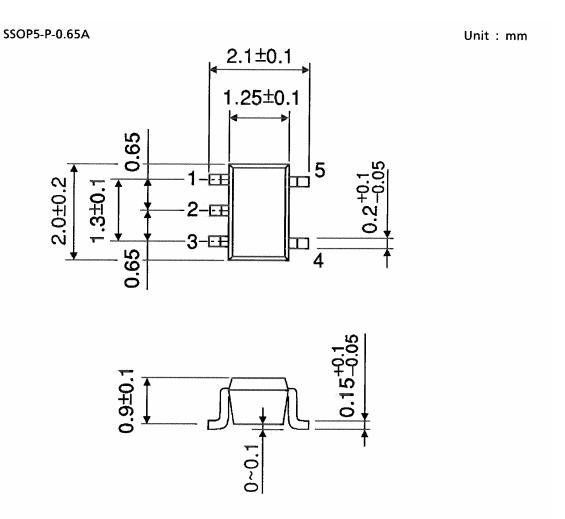


Figure 3  $t_{pLZ}$ ,  $t_{pHZ}$ ,  $t_{pZL}$ ,  $t_{pZH}$ 

# **Package Dimensions**



Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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